

FIG.2

, BLK	∠BLK (DATA AREA) 30	, BLK	BLK (SUBETITUTION AREA)31	BLK	
4			1		VIIIII
ECC FOR "M0 "	ECC FOR	ECC FOR "Mn "	ECC FOR "MX "		
MANAGEMENT INFORMATION "MO" IN SECTOR ADDRESS "0"	OR "St" MANAGEMENT INFORMATION "MK" IN SECTOR ADDRESS "K" DEFECT SECTOR	MANAGEMENT INFORMATION "Mn" IN SECTOR ADDRESS "n"	MANAGEMENT INFORMATION "Mx" IN SECTOR ADDRESS "x"		AGEMENT TABLE
ECC FOR "SO"	ECC FOR "St" DEFECT	ECC FOR "Sn"	ECC FOR "SK"		ON AREA MANA
SECTOR DATA "S0" OF 512 BYTES	SECTOR DATA "SK" OF 512 BYTES	SECTOR DATA "Sn" OF 512 BYTES	SECTOR DATA "SK" OF 512 BYTES	PARAMETER SECTOR	SUBETITUTION
(SECTOR ADDRESS)	(SECTOR ADDRESS "K"	(SECTOR ADDRESS "n"	(SECTOR ADDRESS "x"	32	33

FIG.3

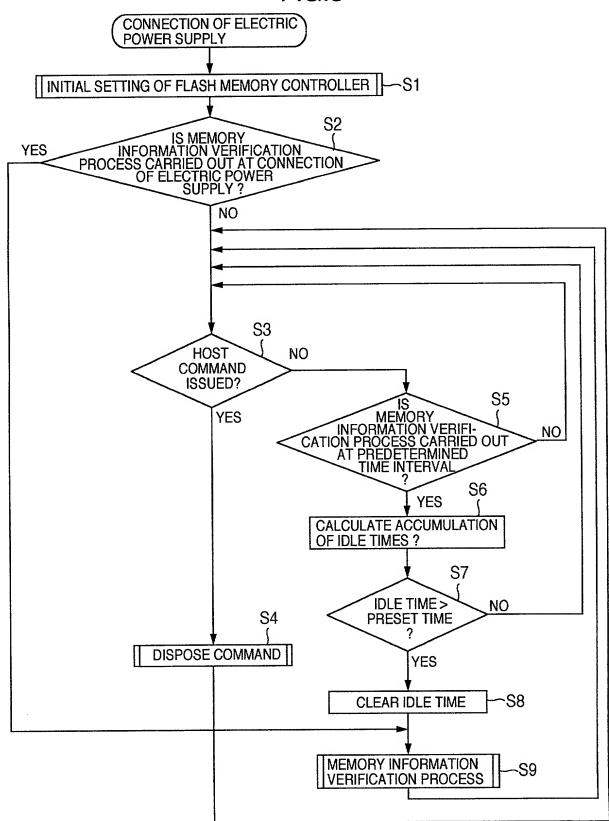


FIG.4

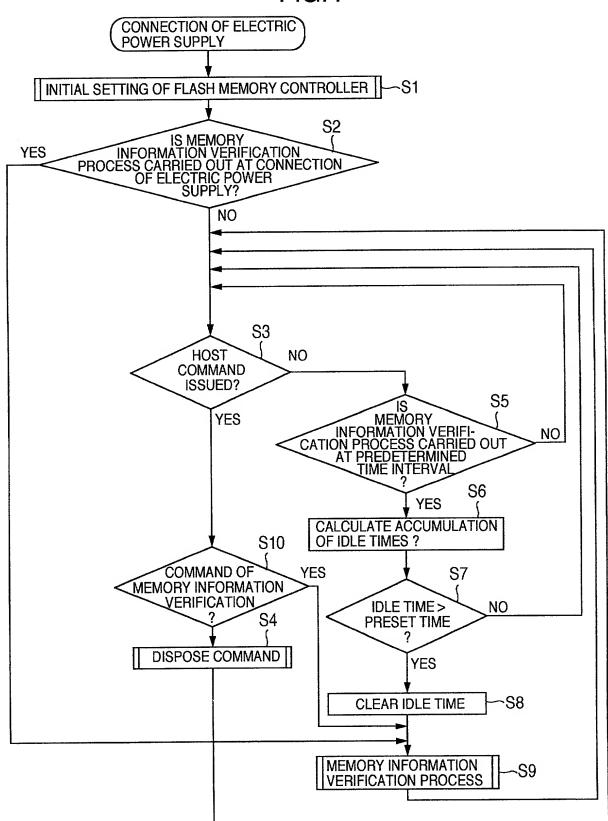
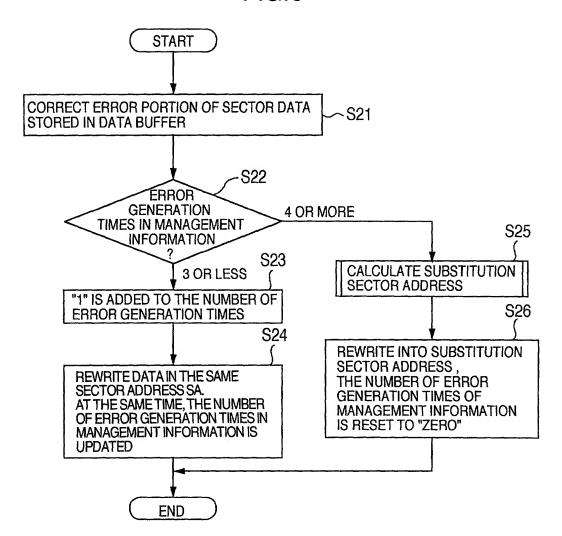


FIG.5 **START** INITIALIZE SECTOR ADDRESS OF FLASH MEMORY ~S11 **S12** READ ONE SECTOR DATA FROM SA INTO DATA BUFFE AT THE SAME TIME, DETECT ERROR IN SECTOR DATA BY ECC CIRCUIT **S13** YES **ERROR S14** EXISTS ? PROCESS FOR ERROR CORRECTION NO S15 PROCESS FOR **REWRITTING DATA S16** YES SA=MAXIMUM VALUE? **END** NO SA=SA+1 S17 **S18** YES IS HOST COMMAND ISSUED? S19 ŃΟ MEMORY YES **INFORMATION VERIFICATION** COMMAND S20 NO DISPOSE OTHER COMMANDS

FIG.6



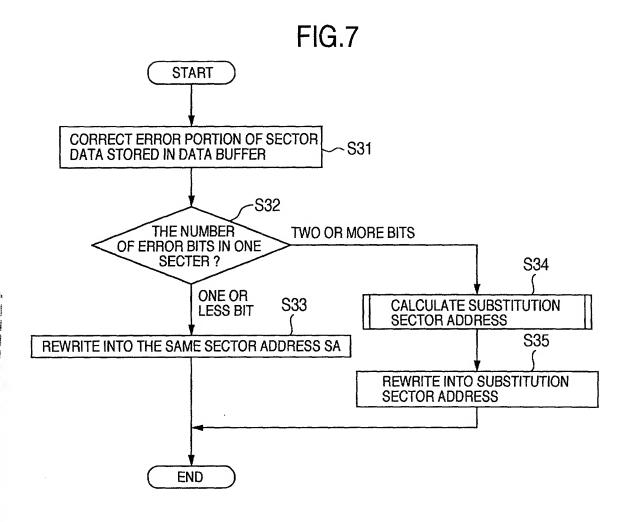


FIG.8

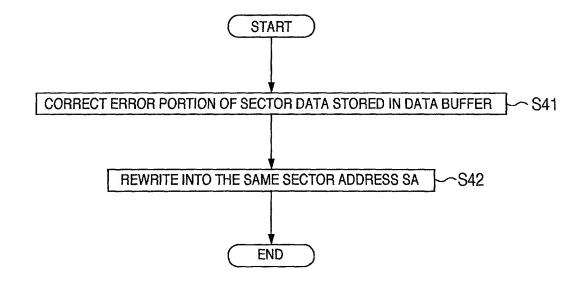


FIG.9

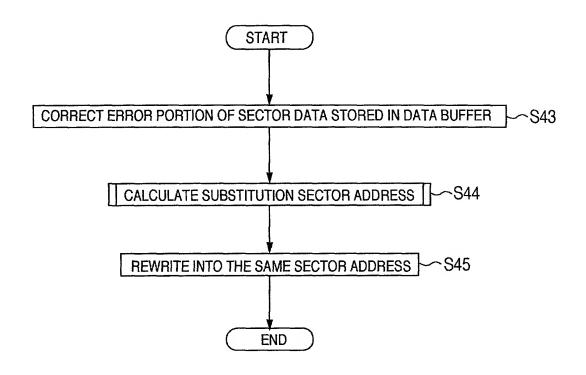


FIG.10

